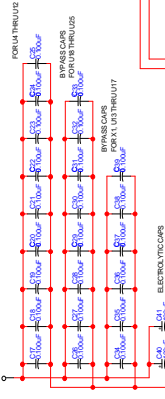


SI-C31PCI R3



STATUS BIT	ENABLE	DISABLE
BIT0: CLK TEST	ON	OFF
BIT1: DATA ADDRESS	ON	OFF
BIT2: DATA ADDRESS	ON	OFF
BIT3: DATA ADDRESS	ON	OFF
BIT4: DATA ADDRESS	ON	OFF
BIT5: DATA ADDRESS	ON	OFF
BIT6: DATA ADDRESS	ON	OFF
BIT7: DATA ADDRESS	ON	OFF
BIT8: DATA ADDRESS	ON	OFF
BIT9: DATA ADDRESS	ON	OFF
BIT10: DATA ADDRESS	ON	OFF
BIT11: DATA ADDRESS	ON	OFF
BIT12: DATA ADDRESS	ON	OFF
BIT13: DATA ADDRESS	ON	OFF
BIT14: DATA ADDRESS	ON	OFF
BIT15: DATA ADDRESS	ON	OFF
BIT16: DATA ADDRESS	ON	OFF
BIT17: DATA ADDRESS	ON	OFF
BIT18: DATA ADDRESS	ON	OFF
BIT19: DATA ADDRESS	ON	OFF
BIT20: DATA ADDRESS	ON	OFF
BIT21: DATA ADDRESS	ON	OFF
BIT22: DATA ADDRESS	ON	OFF
BIT23: DATA ADDRESS	ON	OFF
BIT24: DATA ADDRESS	ON	OFF
BIT25: DATA ADDRESS	ON	OFF
BIT26: DATA ADDRESS	ON	OFF
BIT27: DATA ADDRESS	ON	OFF
BIT28: DATA ADDRESS	ON	OFF
BIT29: DATA ADDRESS	ON	OFF
BIT30: DATA ADDRESS	ON	OFF
BIT31: DATA ADDRESS	ON	OFF

